

**AMENDMENTS TO THE SPECIFICATION**

Please amend the specification as set forth below in marked-up form.

Please amend the paragraph beginning on Page 8, line 14 as follows:

The minimum number of address pairs required for determining a redundant line to be used for repairing an abnormal memory cell is explained by giving an example as follows.

Assume that the number of redundant lines connected in the row direction is  $m$  and the number of redundant lines connected in the column direction is  $n$ . In this case, it is sufficient to provide the first storage means with a buffer having a size of  ~~$m \times n$~~   $2 \times m \times n$ , total, address pairs as a memory for storing the address pairs cited above.